System Management Bus
As the first Intel microprocessor to incorporate a system management bus interface, the Pentium® II Xeon™ processor adds several manageability functions to the Intel product line. Inside the cartridge, two new components (in addition to the thermal sensor) use this interface to communicate with other system management hardware and software.

Processor Information ROM (PI ROM) is a Read-Only Memory (ROM) which contains a wide variety of unique operational specifications, feature data, and tracking information about the individual processor in which it resides. Among the data in the PI ROM are:

- Robust addressing headers to allow for flexible programming and forward compatibility
- Processor QDF/S-spec number and production status bit
- Core information, including CPUID, maximum frequency, voltage, and voltage tolerance
- L2 cache information, including size, number of components, voltage, and voltage tolerance
- Cartridge and substrate revision information
- Thermal reference information for temperature tracking
- Processor core and cartridge feature flags

Scratch EEPROM: The Pentium II Xeon processor also contains an Electrically Erasable and Programmable Read-Only Memory (EEPROM) device which contains no data when shipped from Intel's factory. System manufacturers or processor resellers have the option to include whatever data they wish in this ROM. It can also be used by a system to track various information about the system or the processor, including system specifications, inventory and service tracking, installation defaults, environment monitoring, usage data, or anything else the system manufacturer finds useful.